

FIG. 1

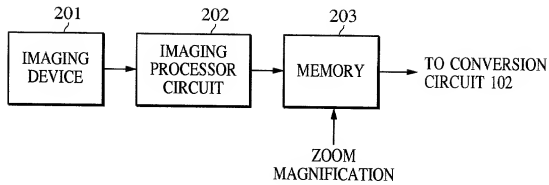


FIG. 2A

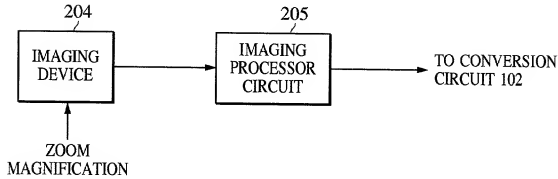


FIG. 2B

```

graph LR
    101[FROM IMAGING CIRCUIT 101] -- "H x W" --> 301[ARBITRARY-MAGNIFICATION ENLARGING CIRCUIT 301]
    MAG[MAGNIFICATION] --> 301
    301 -- "P x Q" --> 302[FIXED-FACTOR REDUCING CIRCUIT 302]
    302 -- "M x N" --> 103[TO RECORDING PROCESSOR CIRCUIT 103]
  
```

0987253 = 0606014

```

graph LR
    101[FROM IMAGING CIRCUIT 101] -- "H x W" --> 301[ARBITRARY-MAGNIFICATION ENLARGING CIRCUIT 301]
    MAG[MAGNIFICATION] --> 301
    301 -- "P x Q" --> 302[FIXED-FACTOR REDUCING CIRCUIT 302]
    302 -- "901a M x N" --> 103[TO RECORDING PROCESSOR CIRCUIT 103]
    302 -- "901b" --> MAG
  
```

FIG. 10

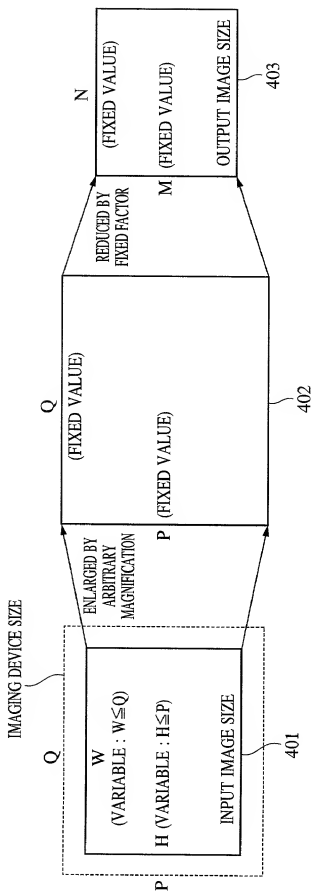


FIG. 4

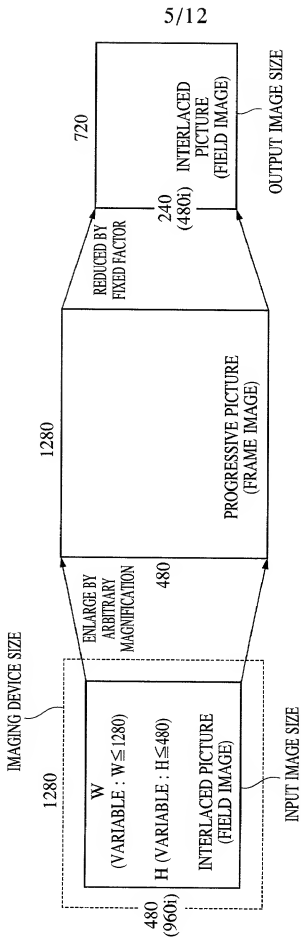


FIG. 5

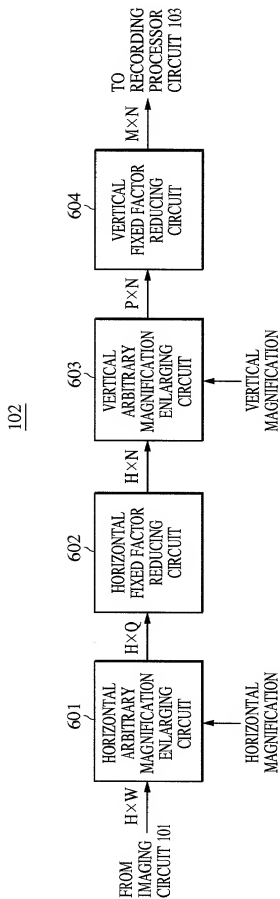
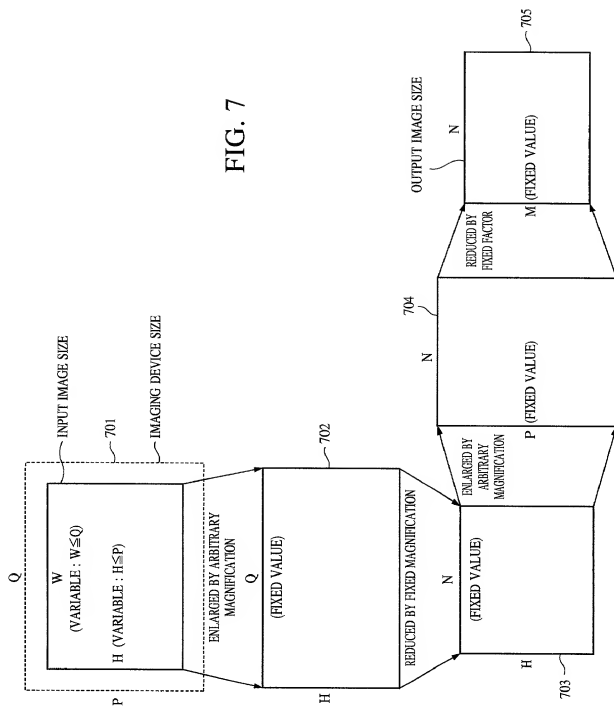


FIG. 6



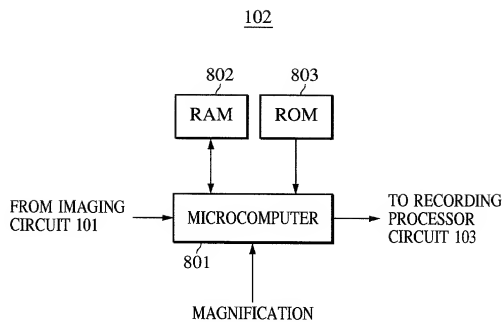


FIG. 8

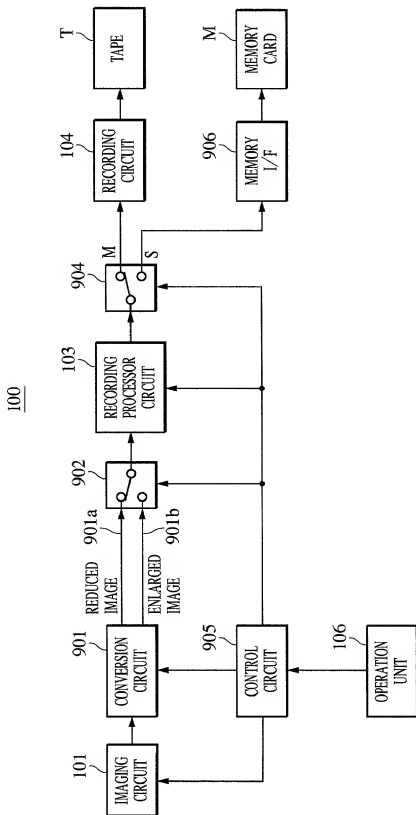


FIG. 9

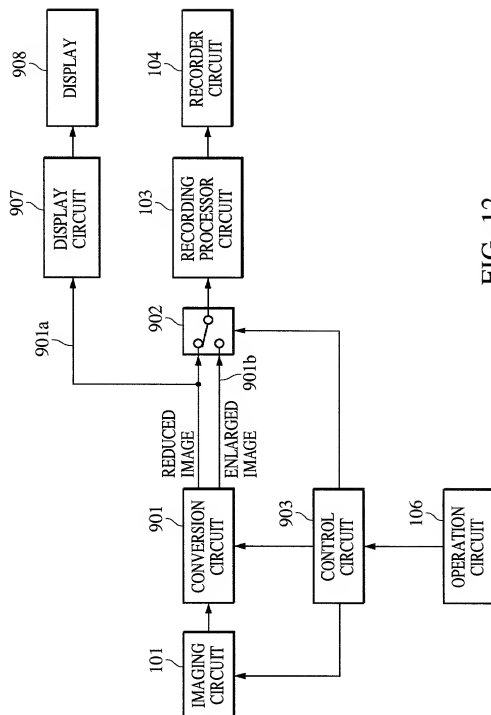


FIG. 12

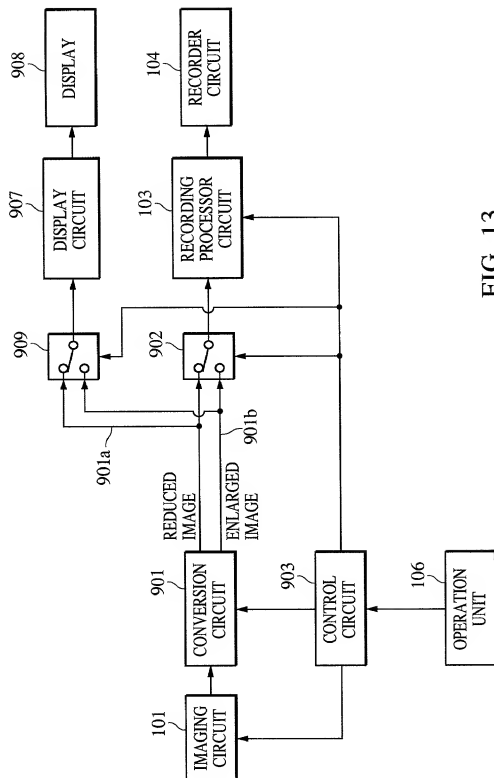


FIG. 13